



**Press Release - November 17th, 2009**

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**CAPS announces support for NVIDIA® Fermi™ computing within its HMPP™ compiler.**

CAPS entreprise the industry leader in development tools for high performance applications is pleased to announce that it is working closely with the NVIDIA teams to make HMPP compiler fully support the new NVIDIA Fermi architecture.

"With several key features such as a true cache hierarchy, concurrent thread execution and ECC, Fermi brings real breakthrough in GPU computing that we are very excited to exploit within our HMPP directive-based CUDA compiler." declares Dr François Bodin, CAPS Chief Technical Officer.

By generating CUDA code from C and Fortran, HMPP offers a high level abstraction of hybrid programming that fully leverages the computing power of NVIDIA Tesla processors. HMPP enables developers to incrementally write hybrid applications, simply from accelerating regions of code, letting HMPP manage all the optimizations such as CPU-GPU data movements, to advanced tuning CUDA kernels. One of the key benefits of HMPP is to ensure portability and performance of CUDA applications across new versions of NVIDIA processors.

"HMPP enables application developers to reduce the development time required for accelerating legacy code by taking advantage of the massively parallel CUDA architecture of NVIDIA's GPUs", explains Andy Keane, general manager, Tesla GPU computing solutions at NVIDIA. "NVIDIA is collaborating with CAPS to enable them to build a powerful data parallel code generator within HMPP for CUDA GPUs based on the new Fermi architecture."

**About CAPS entreprise**

CAPS entreprise fundamentally gives to software developers an easy access to manycore systems. Its flagship product HMPP™ (Heterogeneous Multicore Parallel Programming) indeed allows a single version of a given application to be developed, ported, maintained and deployed on several manycore systems like those integrating CPUs & GPUs. It unleashes the power of manycore architectures by providing an elegant and pragmatic solution to the porting of legacy software.

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