



CAPS to announce its HMPP™ platform now supports the development of hybrid parallel applications that integrate AMD-ATI R600 based products as hardware accelerators.

Reno, Nevada, November 2007.

CAPS a leading innovator in parallel programming tools announces the availability of its HMPP™ platform to the AMD-ATI R600 based products, extending its portfolio of supported GPUs and FPGAs hardware accelerators. The CAPS engineering team has been experiencing the GPU programming for more than one year with industrial applications that have been accelerated with more than 100x gain performance.

CAPS HMPP™ toolkit is based on a set of compiler directives and comes with development tools and a runtime to simplify the use of hardware acceleration for conventional general purpose applications. The aim of HMPP™ is to integrate the use of hardware accelerators rather than porting applications to make use of them.

While the application control flow runs on the main CPU, the greedy computational parts, written by the CAPS engineering team with AMD-ATI programming environment, are integrated as software plugins and are dynamically managed by HMPP™.

HMPP™ gives programmers a simple, flexible and portable interface for developing parallel applications whose critical computations are distributed at runtime over specialized and possibly heterogeneous cores.

HMPP™ key benefits:

Portability

- Preserve the legacy code.
- Independent from the hardware platform.
- Not locked to a vendor specific API.
- Work with any C and Fortran optimizing compilers.

Scalability

- Distributed computation between CPU and accelerators.
- Integrate with MPI and OpenMP.

Come and see us booth #3242 at Supercomputing in Reno, starting November 10. We will be demonstrating an oil and gas reverse time migration application that has been accelerated with the latest R600 GPU.

Contact: hmpp@caps-entreprise.com
www.caps-entreprise.com
Tel. +33 (0) 299 278 856